

Fig. 1

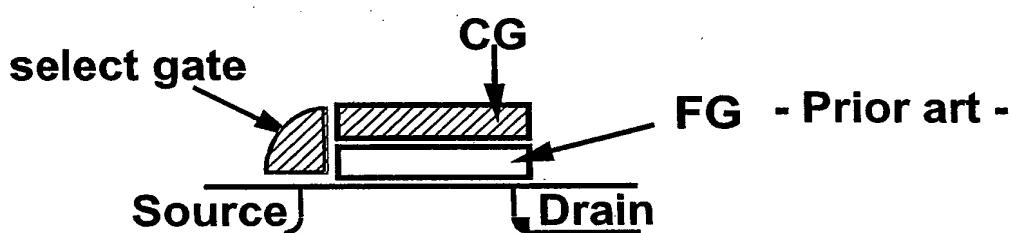


Fig. 2

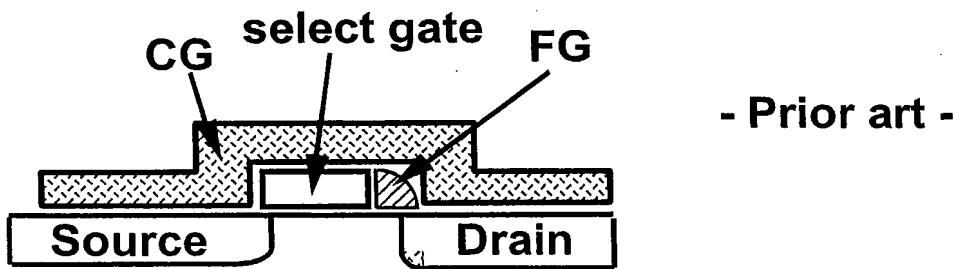


Fig. 3

MULTIBIT NON-VOLATILE MEMORY AND METHOD

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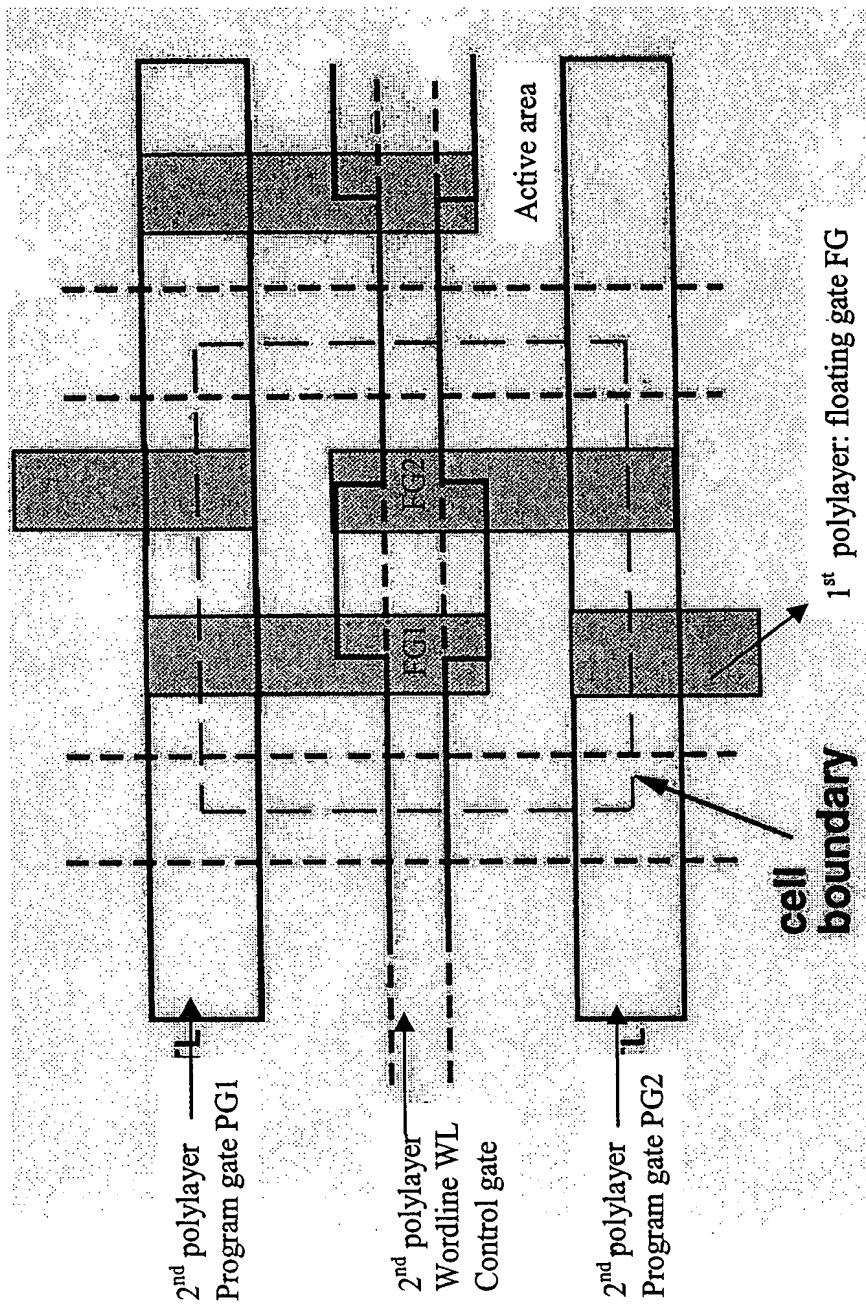


Fig. 4a

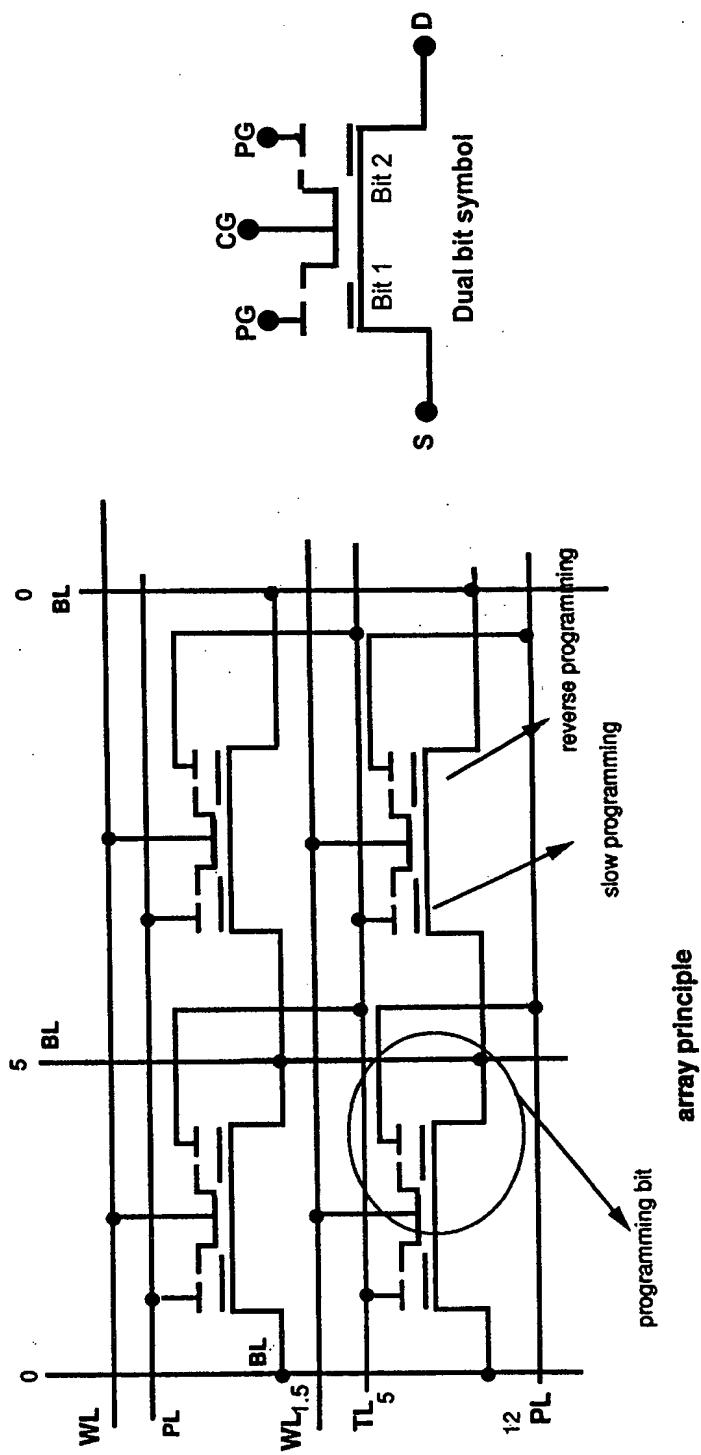


Fig. 4b

MULTIBIT NON-VOLATILE MEMORY AND METHOD

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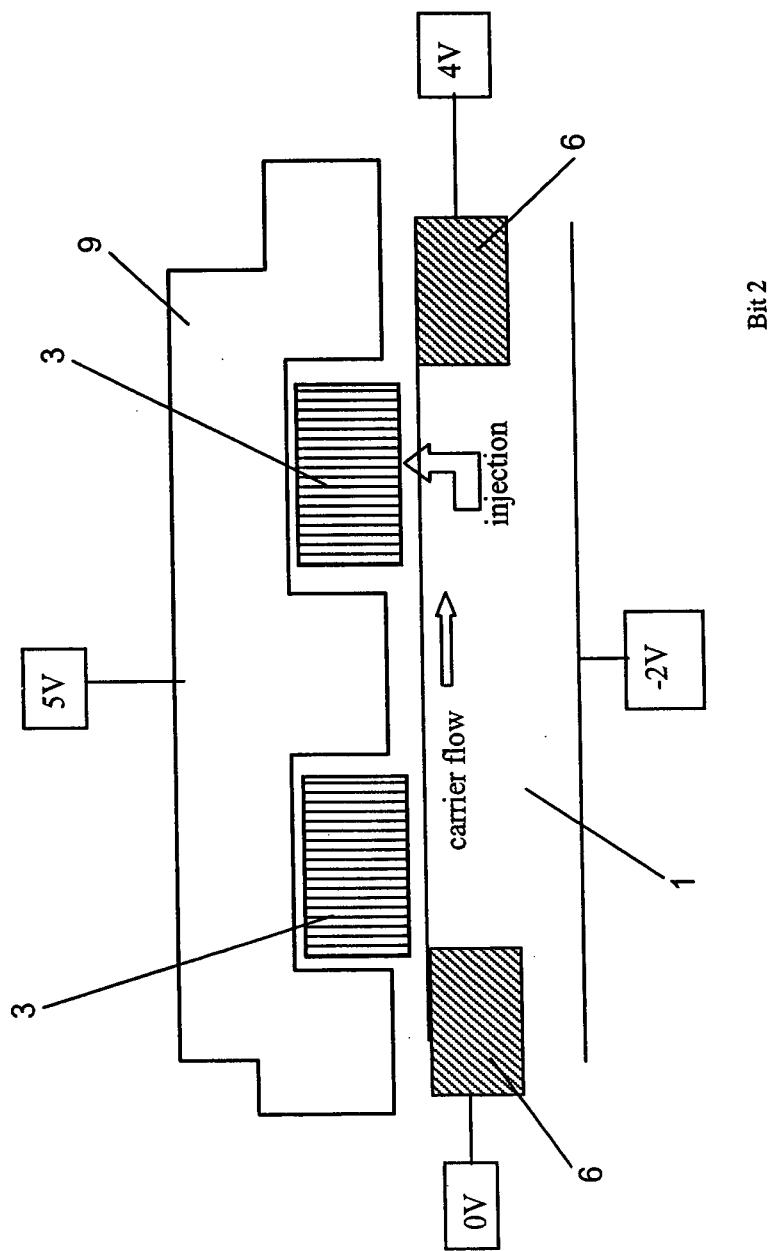


Fig. 5a

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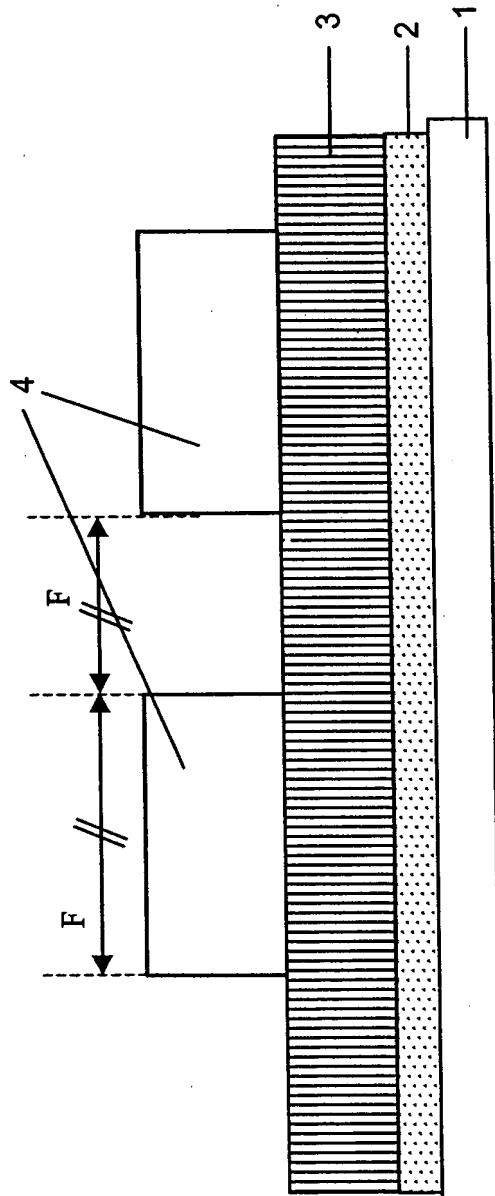


Fig. 5b

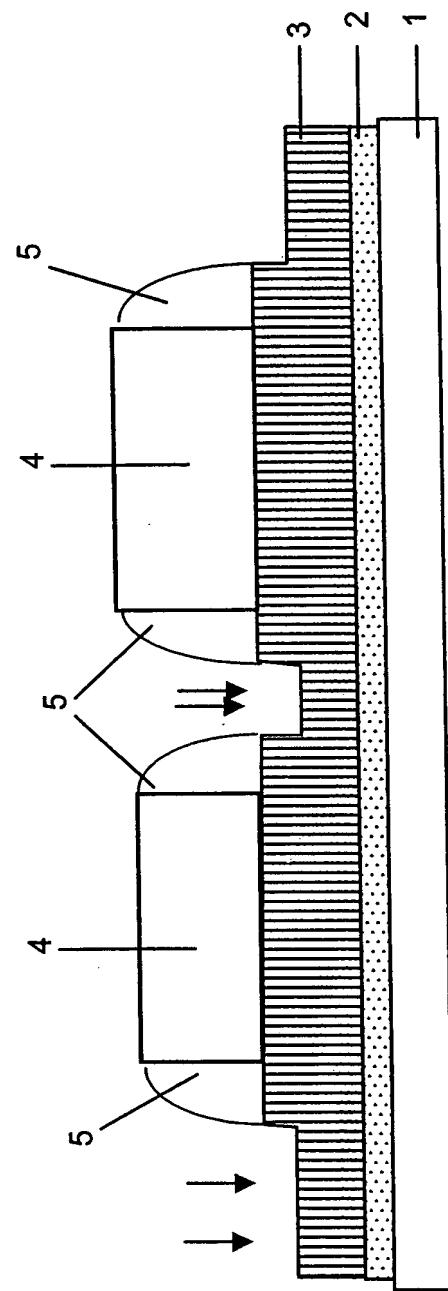


Fig. 5c

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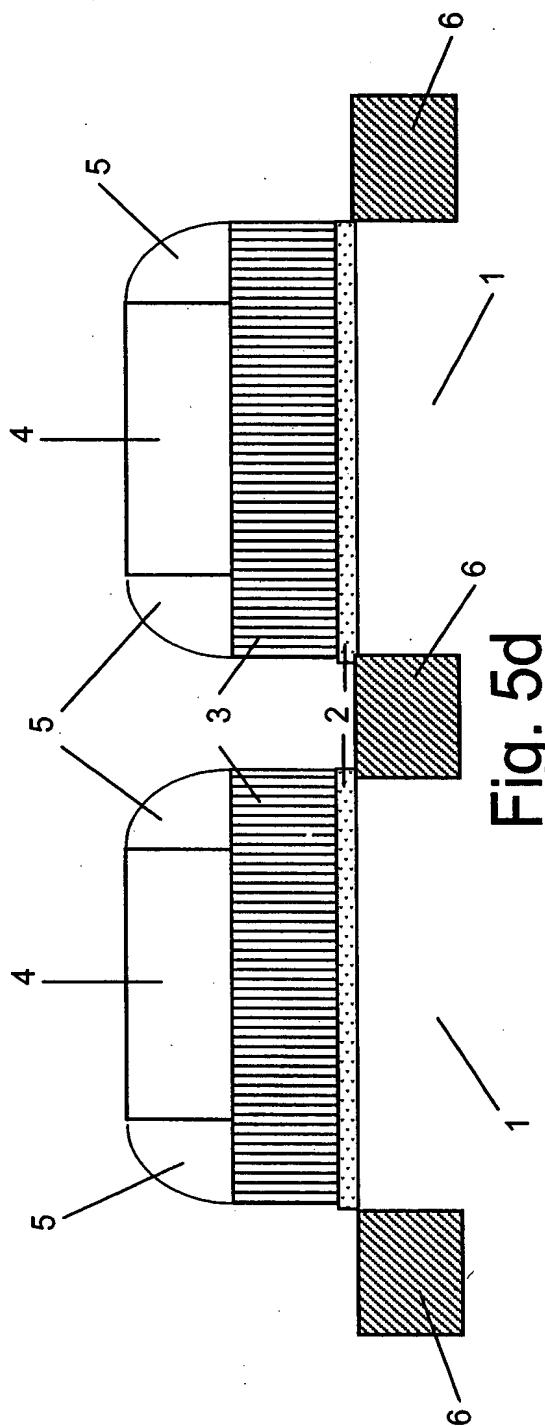


Fig. 5d

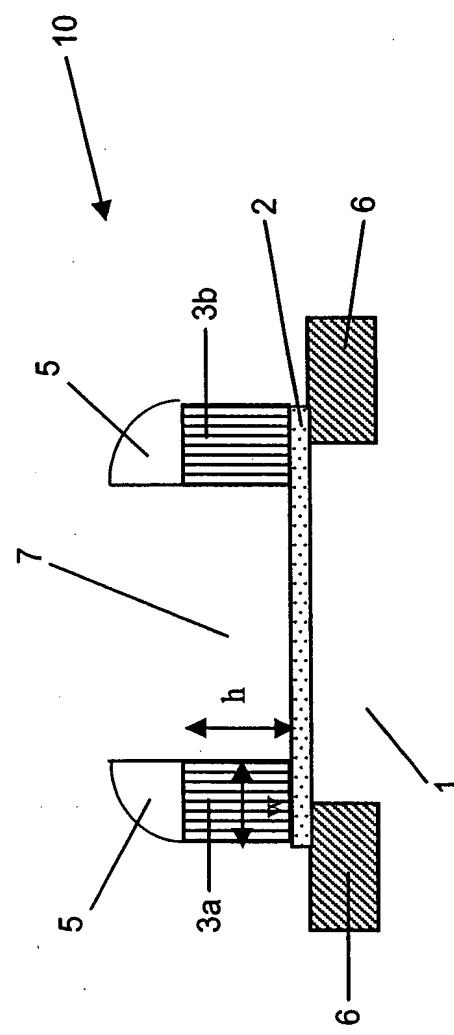


Fig. 5e

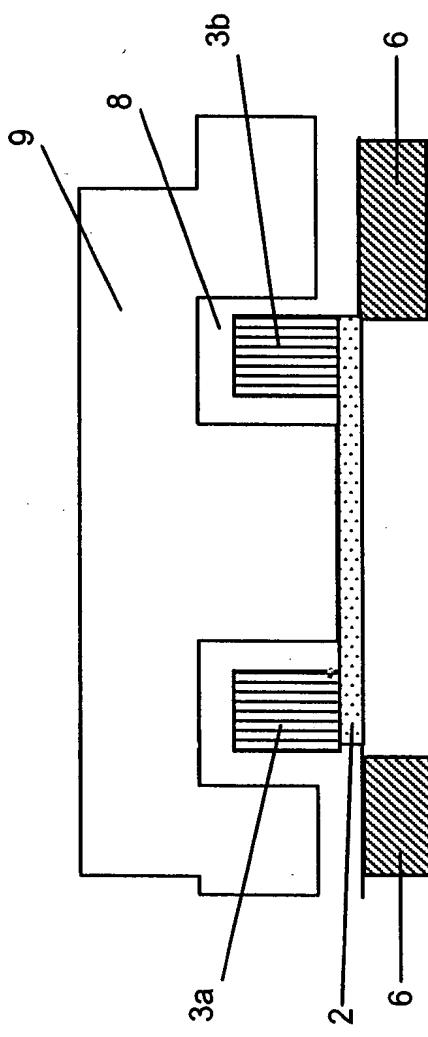


Fig. 5f

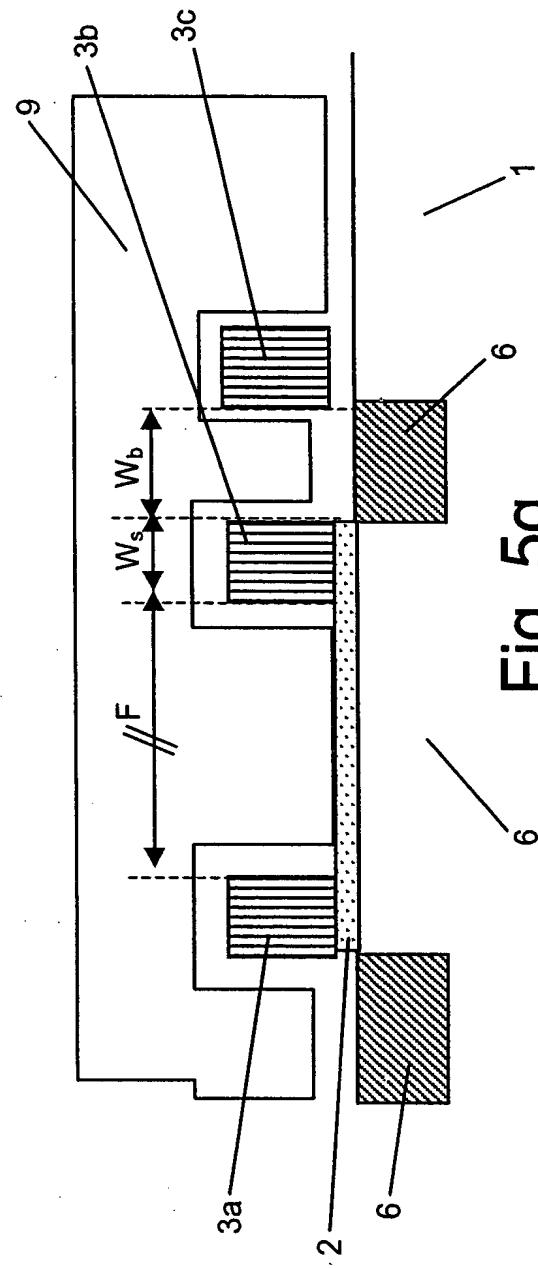


Fig. 5g

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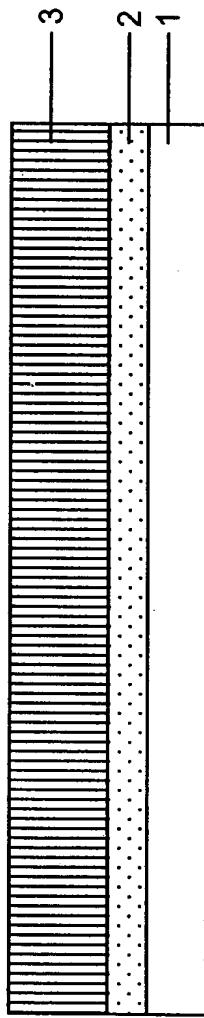


Fig. 5h

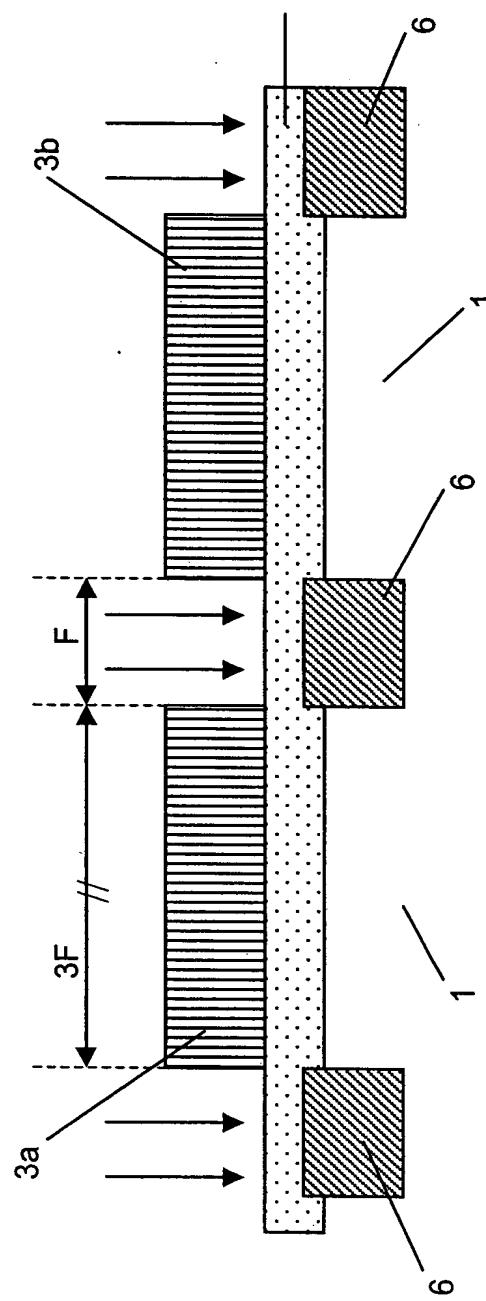


Fig. 5i

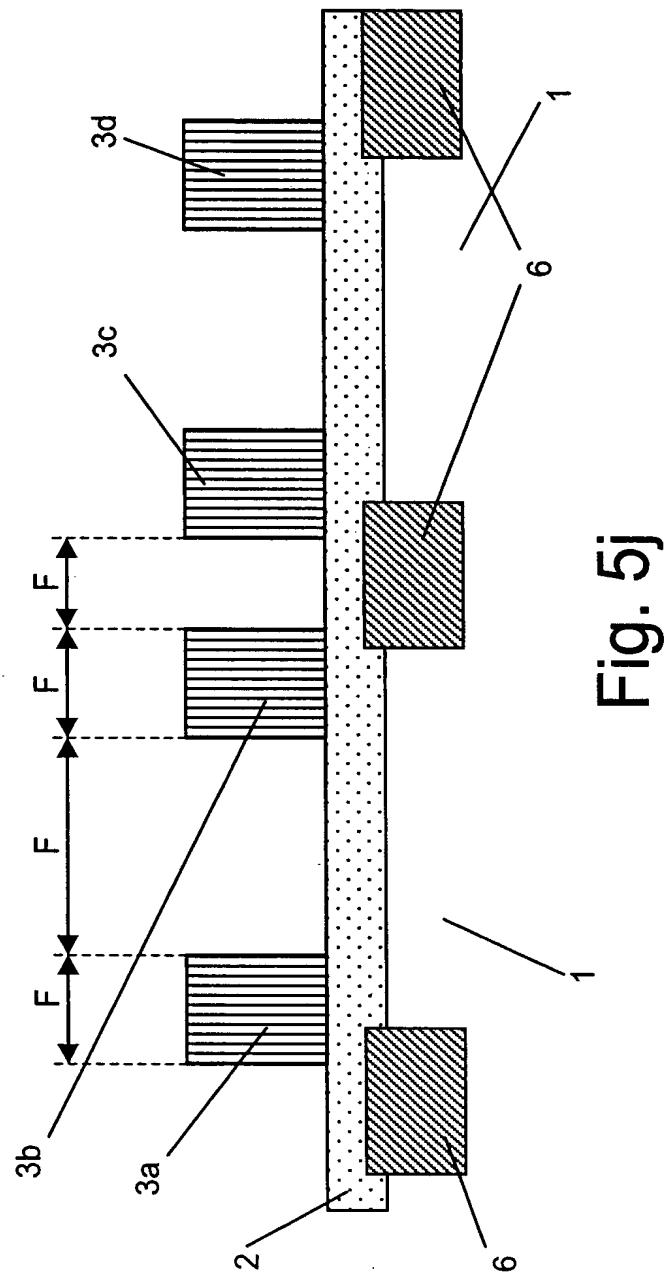


Fig. 5j

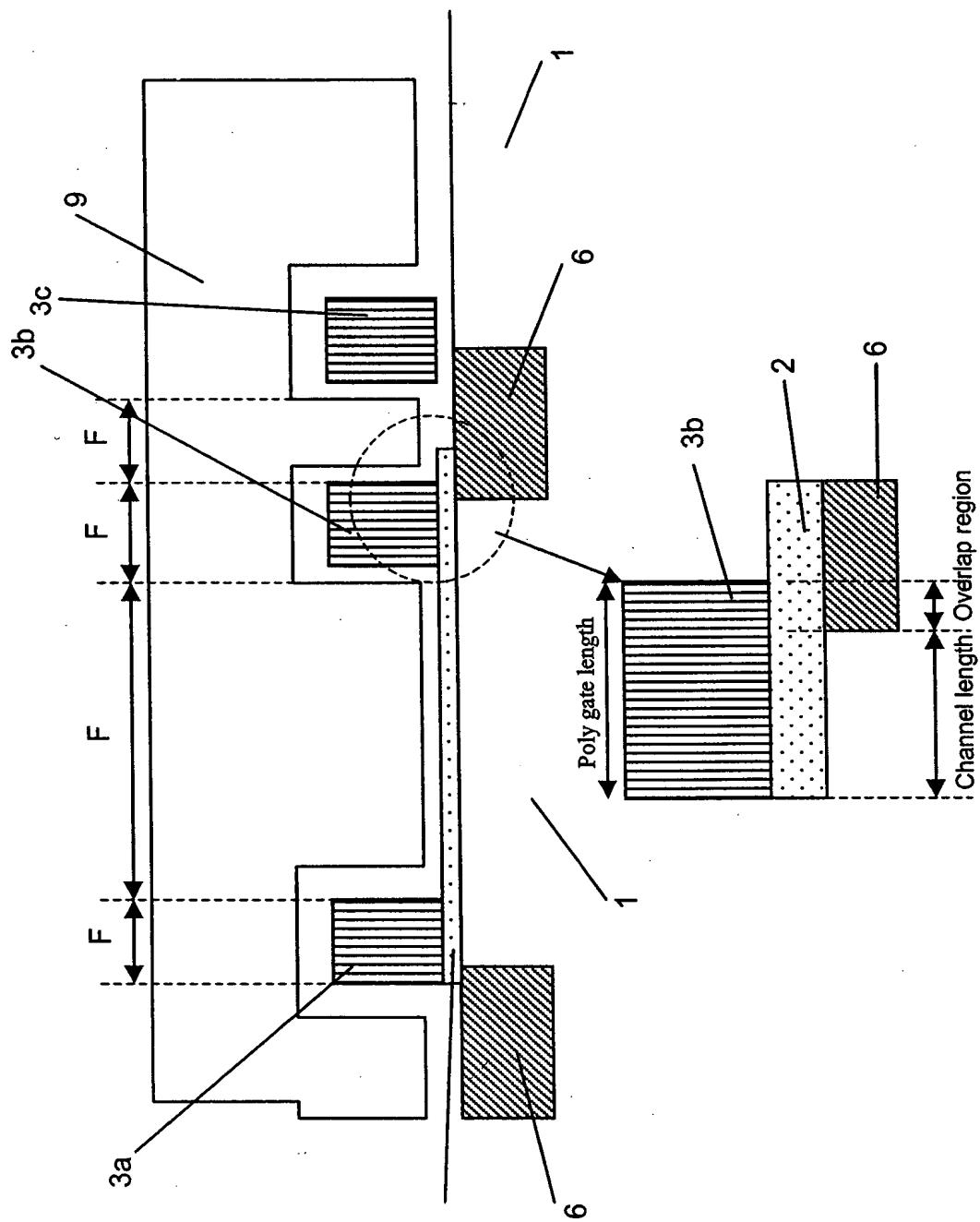


Fig. 5k

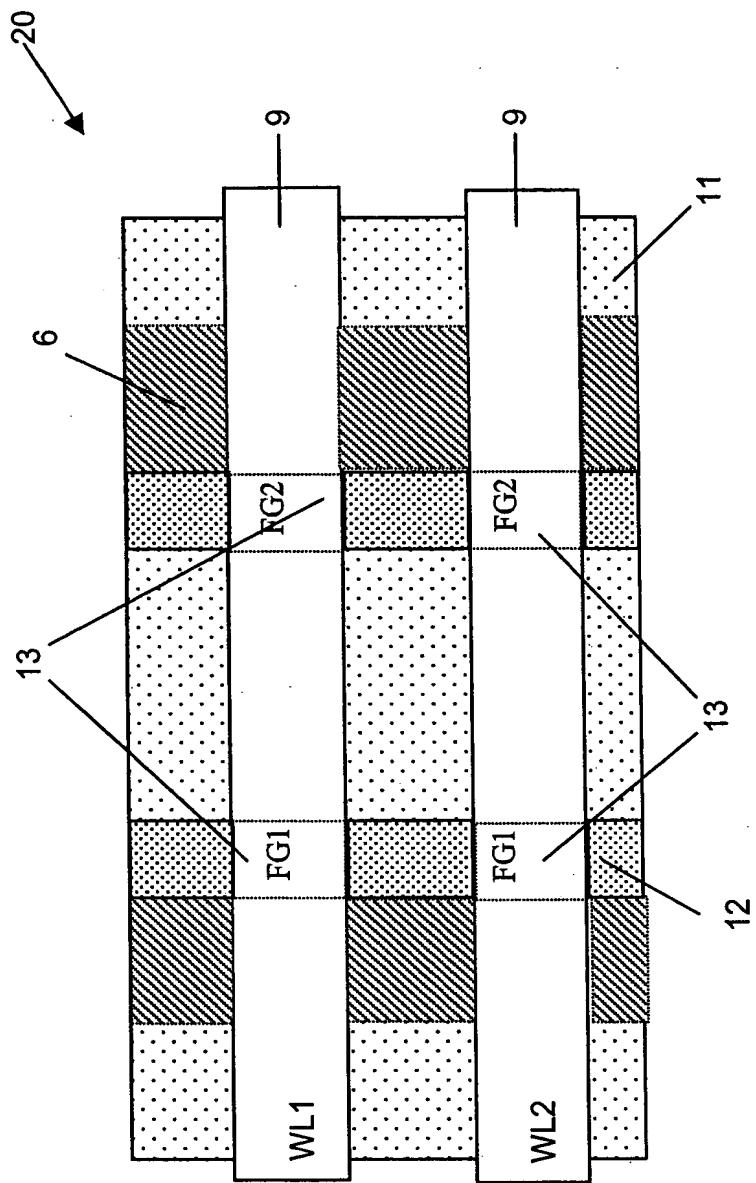


Fig. 5I

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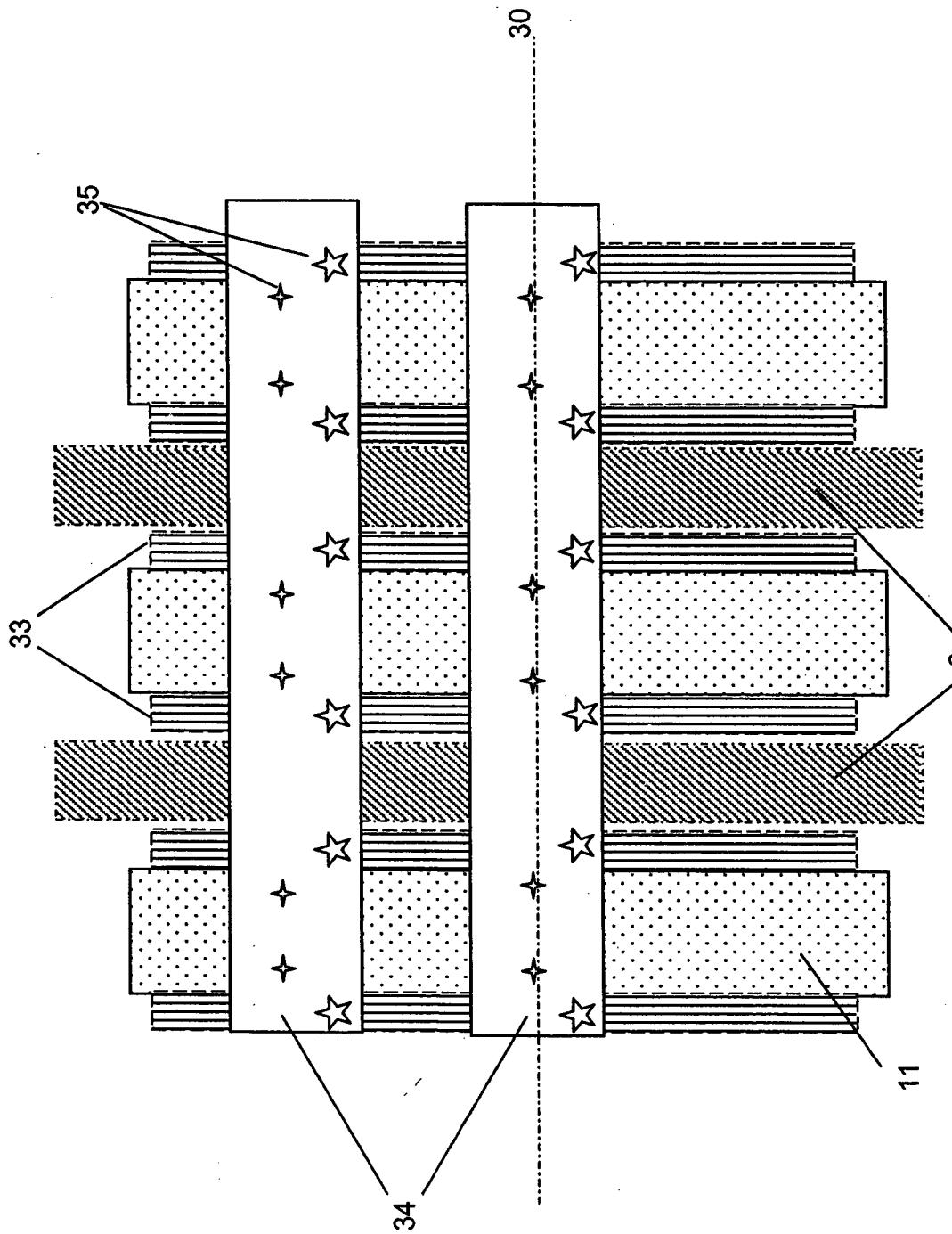


Fig. 6a

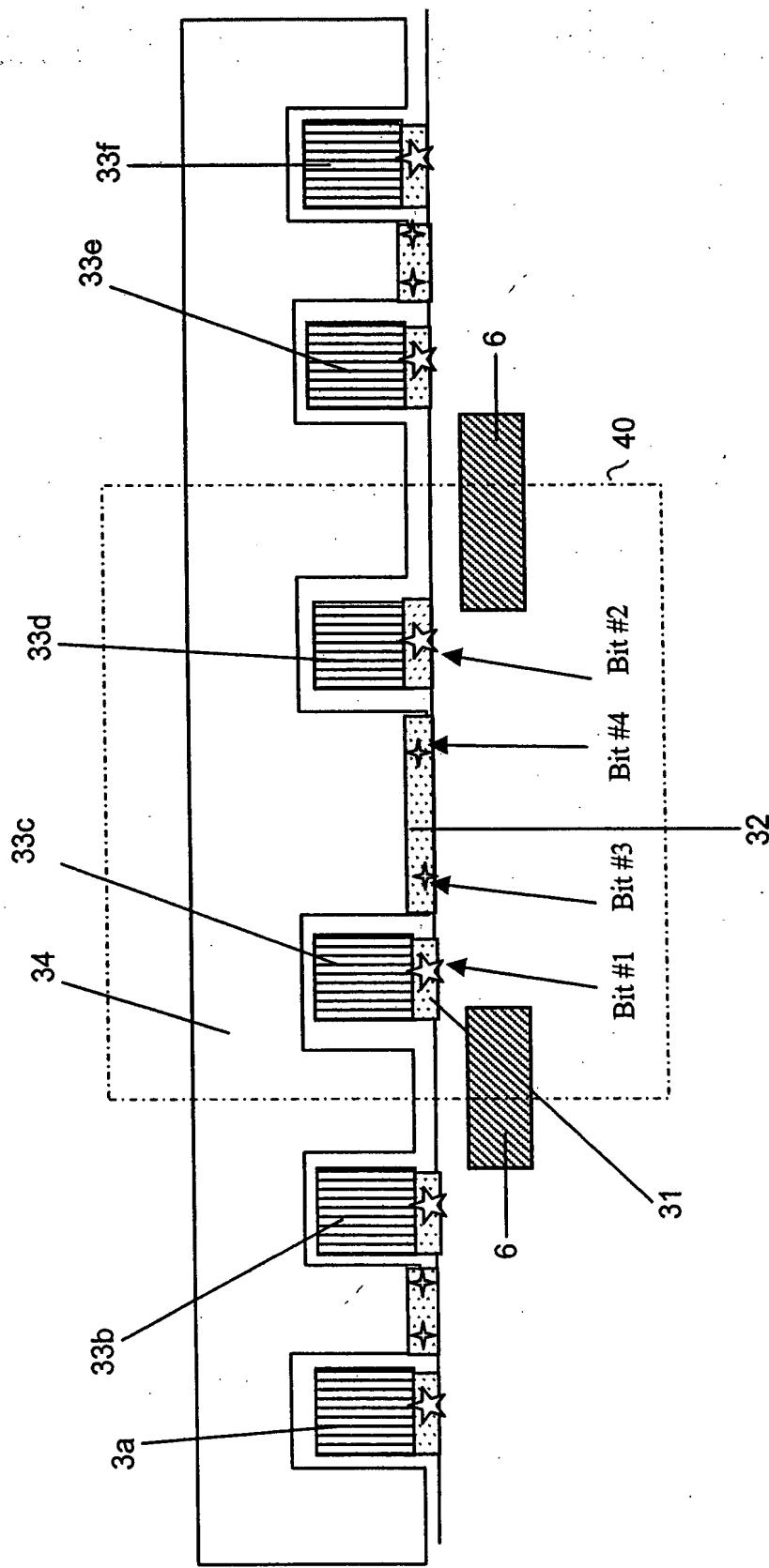


Fig. 6b

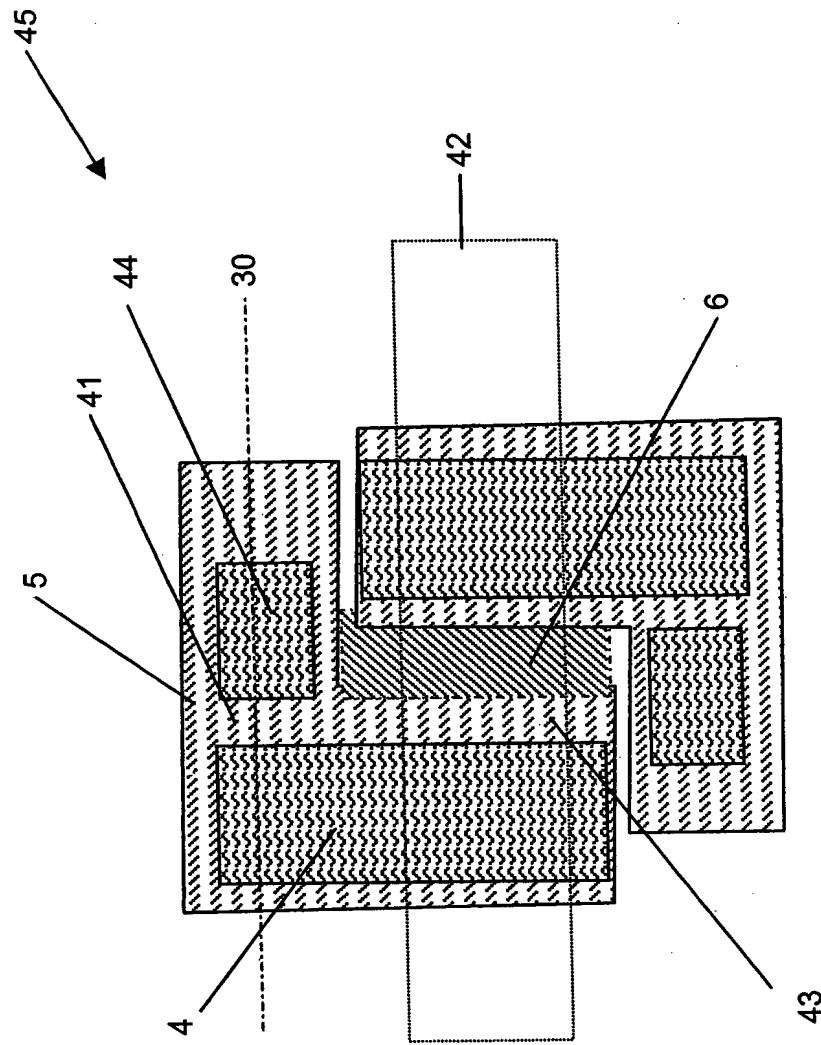


Fig. 7a

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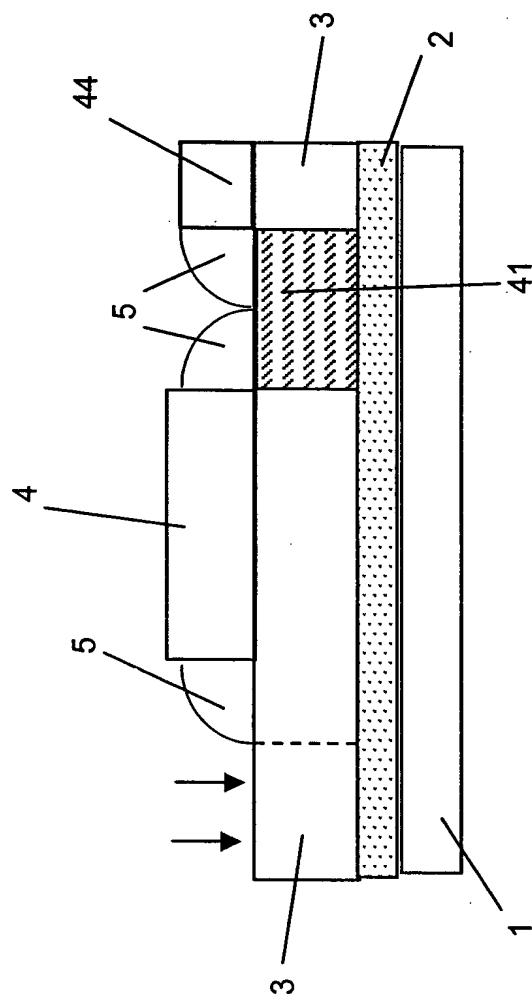


Fig. 7b

MULTIBIT NON-VOLATILE MEMORY AND METHOD

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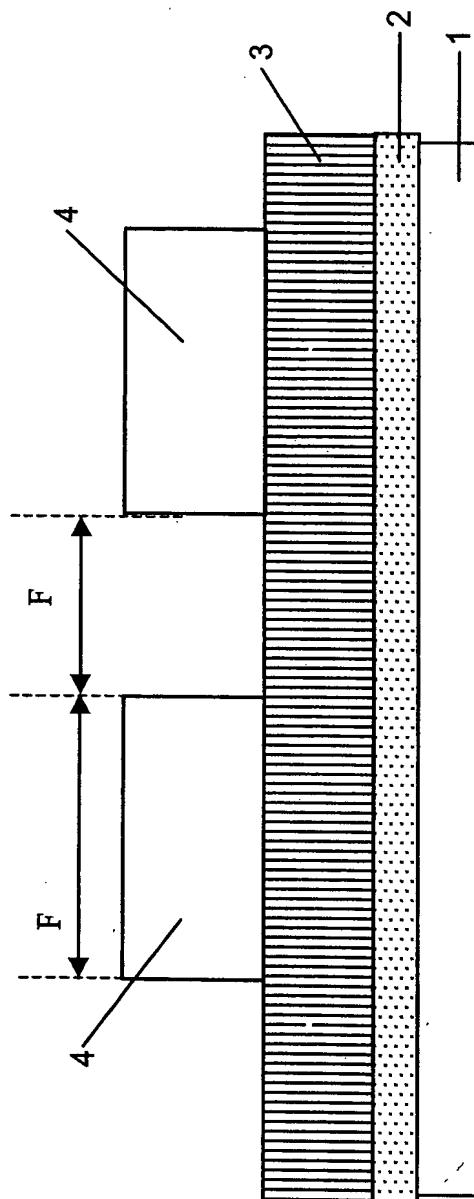


Fig. 8a

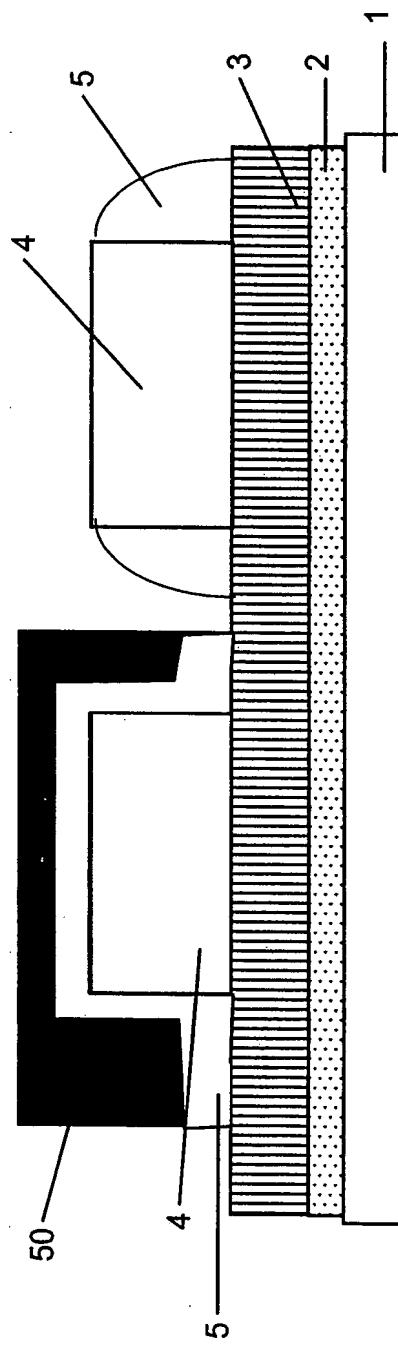


Fig. 8b

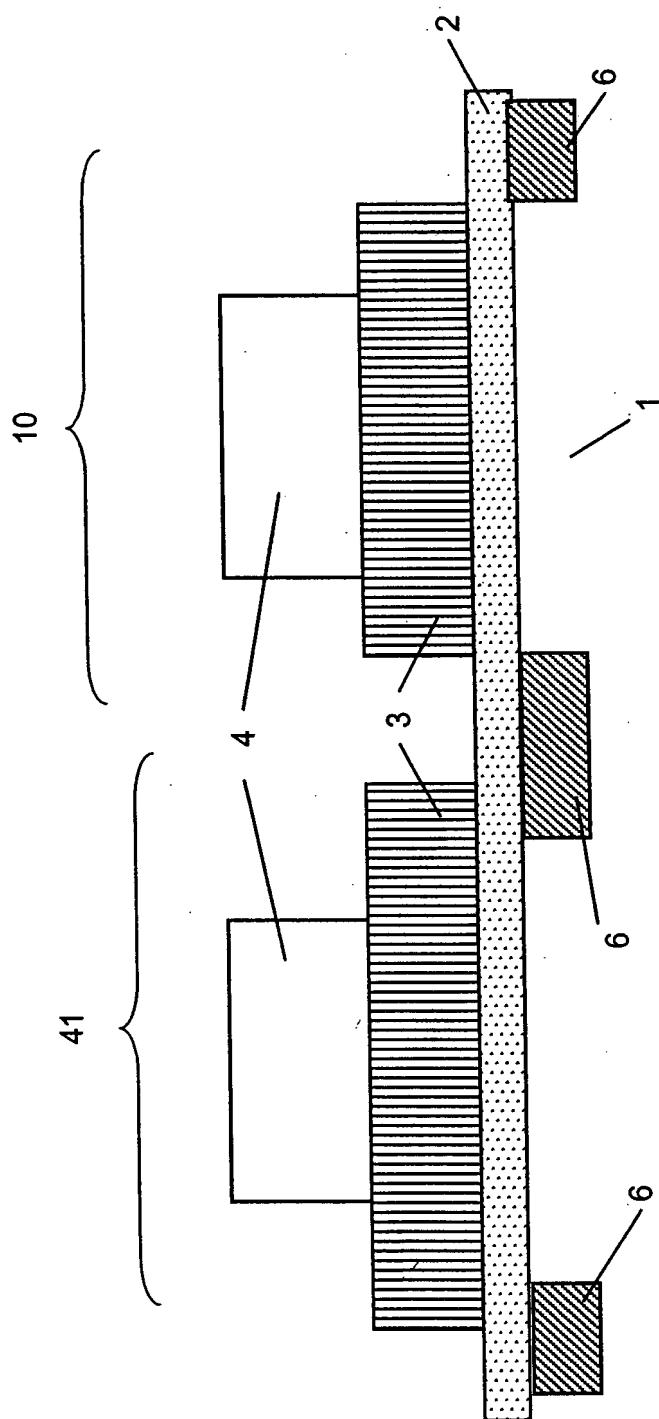


Fig. 8c

MULTIBIT NON-VOLATILE MEMORY AND METHOD
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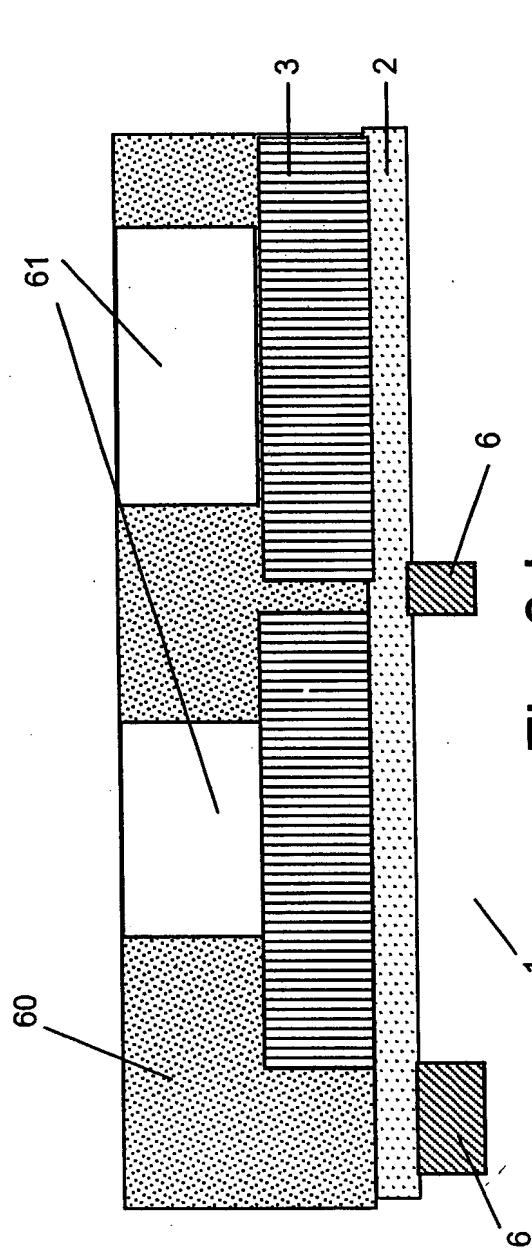


Fig. 8d

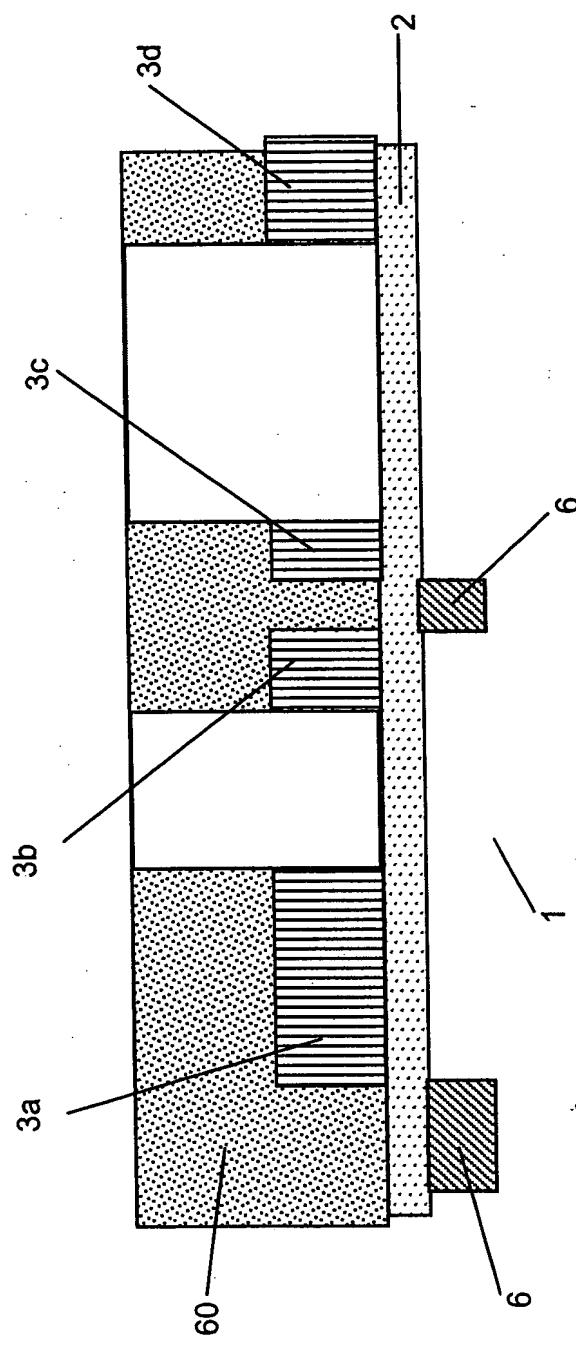


Fig. 8e

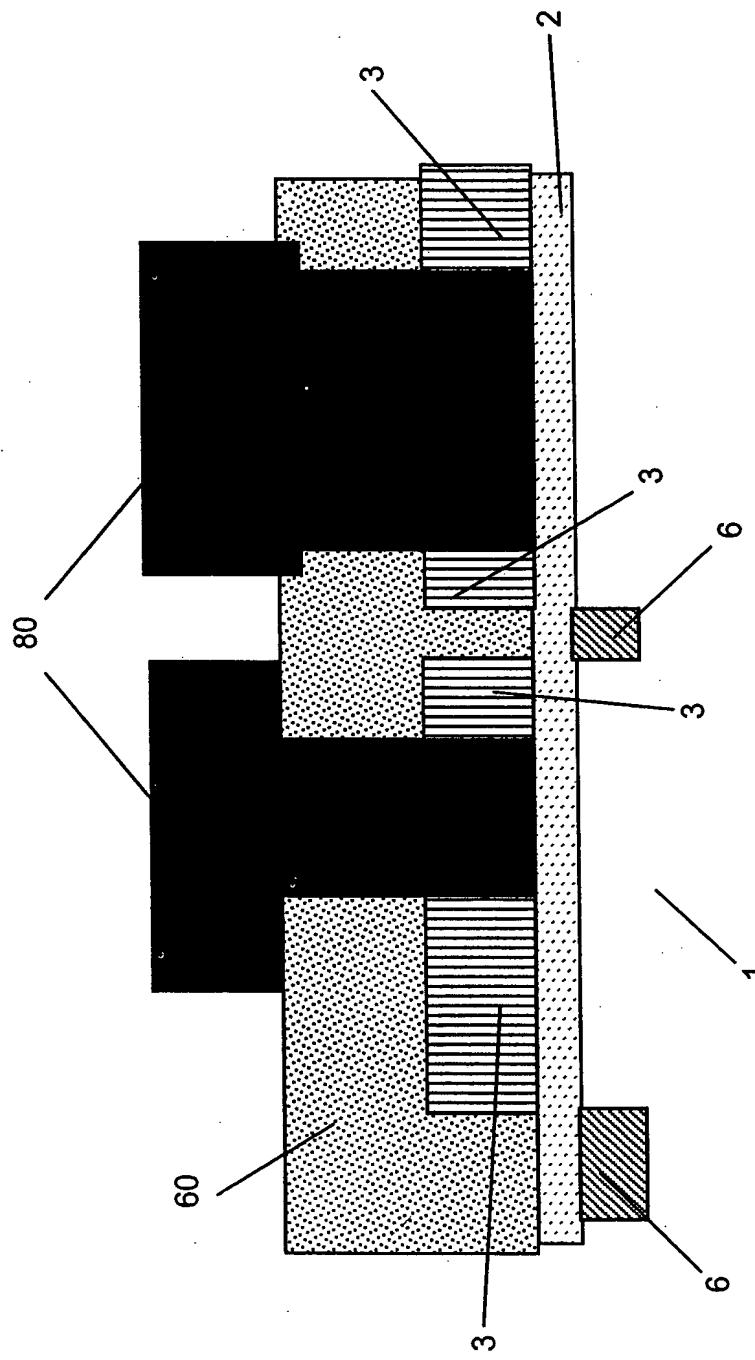


Fig. 8f

MULTIBIT NON-VOLATILE MEMORY AND METHOD

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Appl. No.: Unassigned Atty Docket: IMEC279.001AUS

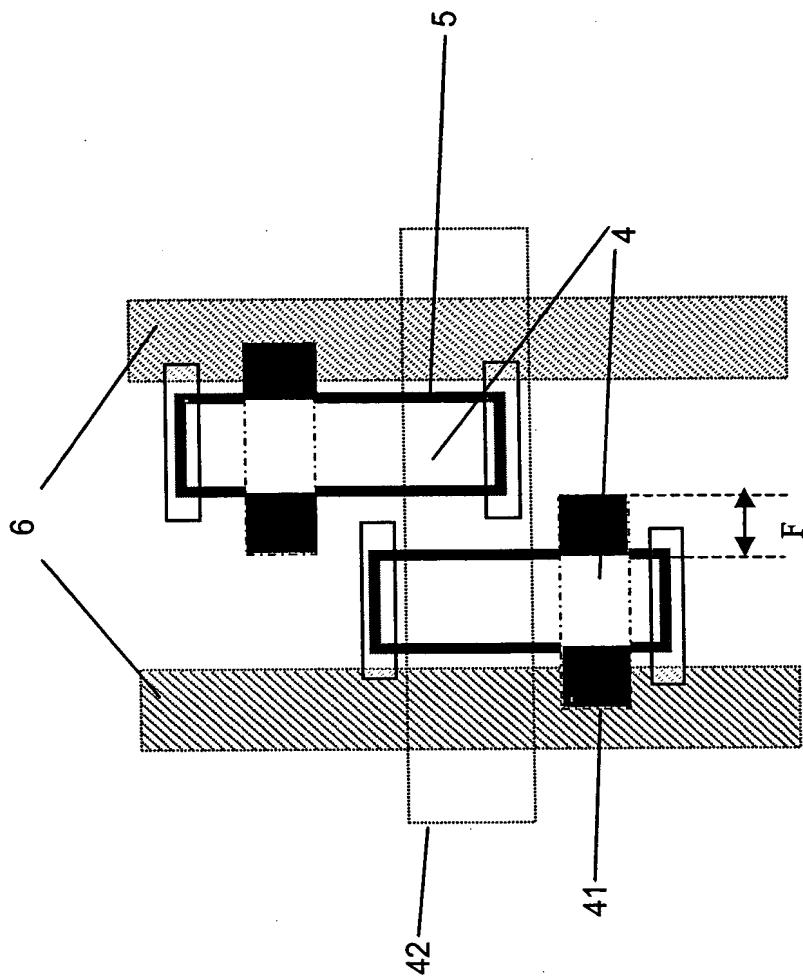


Fig. 9

MULTIBIT NON-VOLATILE MEMORY AND METHOD
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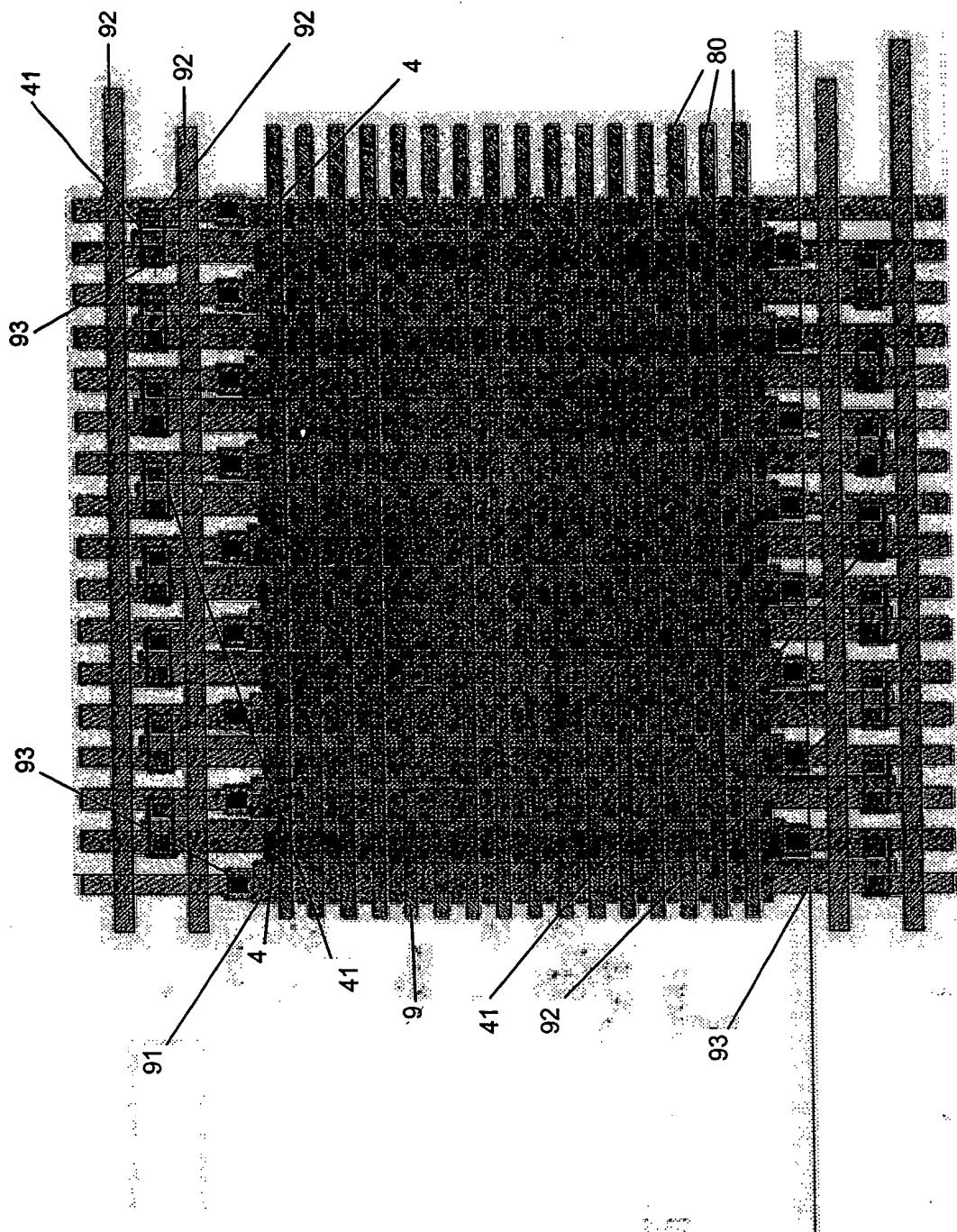


Fig. 10